

AMENDMENTS TO THE CLAIMS (AMENDED SHEETS ATTACHED TO IPER)

This listing of claims will replace all prior versions, and listings, of claims in the application:

1. (original) A method of transmitting video data, comprising the steps of:

encoding a first sequence of video frames;

encoding a second sequence of video frames corresponding to said first sequence of video frames, all video frames in said second sequence predicted from a single reference frame;

transmitting data from said first sequence to a receiver;

on receiving from the receiver an indication that one or more frames in said first sequence is corrupted, transmitting data corresponding to said one or more corrupted frames to the receiver from said second sequence of frames.

2. (original) A method according to claim 1, further comprising:

reverting back to transmitting data from said first sequence after data from the second sequence has been transmitted to the receiver.

3. (original) A method of compensating for transmission errors in a video data signal comprising:

transmitting a first sequence of video frames from a transmitter to a receiver,

detecting one or more corrupted frames in said first sequence;

generating an indication that one or more frames in said first sequence is/are corrupted;

in response to said indication, transmitting frames corresponding to said one or more corrupted frames from a second sequence of video frames, said second sequence corresponding to said first sequence, all video frames in said second sequence predicted from a single reference frame.

WALKER
U.S. National Phase of PCT/GB03/01204

4. (original) A method according to claim 3, further comprising reverting back to transmitting frames from said first sequence after frames have been transmitted to the receiver from the second sequence.

5. (currently amended) A method according to claim 3 ~~or 4~~, wherein the step of detecting corrupted frames is carried out at the receiver.

6. (currently amended) A method according to ~~any of claims 3 to 5~~ claim 3, wherein the step of generating an indication that frames are corrupted is carried out at the receiver.

7. (currently amended) A method according to ~~any of claims 3 to 6~~ claim 3, wherein the step of generating an indication that frames are corrupted includes the receiver generating an indication signal and transmitting the indication signal to the transmitter.

8. (currently amended) A method according to ~~any of claims 3 to 7~~ claim 3, wherein the step of transmitting frames from said second sequence is performed at the transmitter, the transmitted frames from said second sequence being received by the receiver.

9. (currently amended) A storage medium carrying computer readable code representing instructions for causing one or more processors to perform the method according to ~~any of claims 1 to 8~~ claim 1 when the instructions are executed by the processor or processors.

10. (currently amended) A computer program comprising instructions for causing one or more processors to perform the method according to ~~any of claims 1 to 8~~ claim 1 when the instructions are executed by the processor or processors.

11. (currently amended) A computer data signal embodied in a carrier wave and representing instructions for causing one or more processors to perform the method according to ~~any of claims 1 to 8~~ claim 1 when the instructions are executed by the processor or processors.

12. (original) Apparatus for transmitting video data, comprising:

an encoder for encoding a first sequence of video frames, the encoder further arranged for encoding a second sequence of video frames corresponding to said first sequence, all video frames in said second sequence predicted from a single reference frame;

a transmitter for transmitting frames from said first sequence to a receiver;

means for receiving from the receiver an indication that one or more frames in said first sequence is/are corrupted;

wherein said transmitter is arranged in operation to transmit frames corresponding to said one or more corrupted frames from said second sequence to said receiver, upon receiving said indication.

13. (original) Apparatus according to claim 12, the transmitter being further arranged in operation to revert back to transmitting frames from said first sequence after frames have been transmitted to the receiver from the second sequence.

14. (original) A system for compensating for transmission errors in a video data signal comprising:

a transmitter for transmitting a first sequence of video frames;

a receiver for receiving said first sequence;

means for detecting one or more corrupted frames in said first sequence;

means for transmitting frames corresponding to said one or more corrupted frames from a second sequence of video frames, said second sequence corresponding to said first sequence, all video frames in said second sequence predicted from a single reference frame.

15. (original) A system according to claim 14, wherein the means for detecting corrupted frames in said first sequence is at the receiver.

WALKER
U.S. National Phase of PCT/GB03/01204

16. (currently amended) A system according to claim 14 ~~or 15~~, wherein the transmitter is operable to transmit frames from said second sequence to the receiver after detection of one or more corrupted frames in said first sequence.

17. (currently amended) A storage medium carrying computer readable code representing instructions for causing one or more processors to operate as the system according to ~~any of claims 12 to 16~~ claim 12 when the instructions are executed by the processor or processors.

18. (currently amended) A computer program comprising instructions for causing one or more processors to operate as the system according to ~~any of claims 12 to 16~~ claim 12 when the instructions are executed by the processor or processors.

19. (currently amended) A computer data signal embodied in a carrier wave and representing instructions for causing one or more processors to operate as the system according to ~~any of claims 12 to 16~~ claim 12 when the instructions are executed by the processor or processors.